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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/896,467 | 06/29/2001 | Lee D. Whetsel | TI-31203 | 9276 |

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EXAMINER

NGUYEN, JIMMY

| ART UNIT | PAPER NUMBER |
|----------|--------------|
| | 2829 |

DATE MAILED: 04/15/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

| | | | |
|-----------------|--------------|--------------|-----------------|
| Application No. | 09/896,467 | Applicant(s) | WHETSEL, LEE D. |
| Examiner | Jimmy Nguyen | Art Unit | 2829 |
| | | | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 29 June 2001.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1 -11 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1 -11 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s) _____.
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) Notice of Informal Patent Application (PTO-152)
3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____. 6) Other: _____

DETAILED ACTION***Drawings***

1. Figures 1 - 3 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1 –11 are rejected under 35 U.S.C. 102(e) as being anticipated by Kim et al (US 6483758).

As to claim 1, Kim et al disclose (fig 4)

A wafer (180) having a plurality of die (72_1...72_n) to be tested,

A tester (100) having stimulus and response outputs, and

A connection (90, 92) formed between plurality of die and tester outputs.

As to claim 2, Kim et al disclose (fig 4) a process of testing die on wafer, comprising step of

Contacting (throughout the driver 60_1...60_n) the die (72_1....72_n) with a tester (100) , and

Inputting test stimulus and response data from the tester (100).

As to claims 3, 5, Kim et al disclose (fig 4) a process of testing die of wafer comprising;

Probing the wafer (180) using only the number of probe (output from the line 60_1 and 60_2) contacts required for testing a single die, and

Testing a plurality of die (72-1...72_n) simultaneously using only number of probe contacts

As to claim 4, Kim et al disclose (fig 4) a packaged IC test system comprising;

A plurality of packaged Ics (72_1....72_n) to be tested

A tester (100) having stimulus and response outputs, and

A connection (90, 92, 60n) formed between plurality of packaged Ics and tester outputs.

As to claim 6, the structure of the IC is one of Kim et al memory cell (71) (fig 4).

As to claims 7 – 9, Kim et al disclose (fig 4) a process of testing an IC and an tester comprising the step of;

Inputting test stimulus (70_1...70_(2n-1)) and response data (70_2...70_n) to IC (72), and

Reading pass/fail information (from the tester 100) from the IC (72n).

As to claims 10, 11, Kim et al disclose (fig 4) a semiconductor wafer comprising;

A plurality of identical die (72n) formed on the wafer (180), each die (72) having a common set of I/O pads , and

A plurality of connections (90, 92) formed on the wafer (180), ones of plurality of connections (90) forming unique electrical connections between common die input pads 70_1..70_2n) and unique electrical connections (92) between common die output pads (70_2...70_2n).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jimmy Nguyen at (703) 306-5858. Any inquiry of a general nature of relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-4900.

JN.
April 5, 2003

Vinh P. Nguyen
VINH P. NGUYEN
PRIMARY EXAMINER
GROUP 2829
04/07/03